SIEMENS

Data sheet

6ES7216-2AD23-0XB0

Spare part SIMATIC S7-200, CPU 226 Compact unit, DC power supply 24 DI DC/16 DO DC, 16/24 KB progr./10 KB data, 2 PPI/user-programmable interface



Figure similar

Supply voltage	
Rated value (DC)	
• 24 V DC	Yes
Load voltage L+	
• Rated value (DC)	24 V
 permissible range, lower limit (DC) 	20.4 V
 permissible range, upper limit (DC) 	28.8 V

Input current	
Inrush current, max.	10 A; at 28.8 V
from supply voltage L+, max.	1 050 mA; 150 mA to 1 050 mA output current for expansion
	modules (5 V DC) 1 000 mA

Encoder supply 24 V encoder supply • 24 V • Short-circuit protection • Output current, max. Encoder supply Yes; permissible range: 15.4 to 28.8 V Yes; electronic at 400 mA

Power loss	
Power loss, typ.	11 W
Memory	
Number of memory modules (optional)	1; pluggable memory module, content identical with integral EEPROM; can additionally store recipes, data logs and other files
Work memory	
• integrated (for program)	24 kbyte; 16 KB with active run-time edit
• integrated (for data)	10 kbyte
Backup	
• present	Yes; Program: Entire program maintenance-free on integral EEPROM, programmable via CPU; data: Entire DB 1 loaded from PG/PC maintenance-free on integral EEPROM, current values of DB 1 in RAM, retentive memory bits, timers, counters, etc. maintenance-free via high-performance capacitor; optional battery for long-term buffering
Battery	
Backup battery	
Backup time, max.	100 h; (min. 70 h at 40 °C); 200 days (typ.) with optional battery module
CPU processing times	
for bit operations, max.	0.22 μs
Counters, timers and their retentivity	
S7 counter	
Number	256
Retentivity	
— adjustable	Yes; via high-performance capacitor or battery
— lower limit	1
— upper limit	256
Counting range	
— lower limit	0
— upper limit	32 767
S7 times	
Number	256
Retentivity	
— adjustable	Yes; via high-performance capacitor or battery
— upper limit	64
Time range	
— lower limit	1 ms
	54 min; 4 timers: 1 ms to 30 s; 16 timers: 10 ms to 5 min; 236
— upper limit	timers: 100 ms to 54 min

Flag	
• Number, max.	32 byte
Retentivity available	Yes; M 0.0 to M 31.7
of which retentive with battery	0 to 255, via high-performance capacitor or battery, adjustable
of which retentive without battery	0 to 112 in EEPROM, adjustable
	, ,
Hardware configuration	
Number of expansion units, max.	7; Only expansion modules of the S7-22x series can be used. Due to the limited output current, the use of expansion modules may be limited.
connectable programming devices/PCs	SIMATIC PG/PC, standard PC
Expansion modules	
 Analog inputs/outputs, max. 	35; max. 28 inputs and 7 outputs (EM) or max. 0 inputs and 14 outputs (EM)
 Digital inputs/outputs, max. 	148; max. 128 inputs and 120 outputs (CPU+EM)
 AS-Interface inputs/outputs, max. 	62; AS-Interface A/B slaves (CP 243-2)
Digital inputs	
Number of digital inputs	24
Source/sink input	Yes; optionally, per group
Input voltage	
● Rated value (DC)	24 V
● for signal "0"	0 to 5 V
● for signal "1"	min. 15 V
Input current	
● for signal "1", typ.	2.5 mA
Input delay (for rated value of input voltage)	
for standard inputs	
— parameterizable	Yes; all
— at "0" to "1", min.	0.2 ms
— at "0" to "1", max.	12.8 ms
for interrupt inputs	
— parameterizable	Yes; I 0.0 to I 0.3
for technological functions	
— parameterizable	Yes; (E 0.0 to E 1.5) 30 kHz
Cable length	
• shielded, max.	500 m; Standard input: 500 m, high-speed counters: 50 m
• unshielded, max.	300 m; not for high-speed signals
Digital outputs	
Number of digital outputs	16; Transistor
Short-circuit protection	No; to be provided externally
Limitation of inductive shutdown voltage to	1 W

on lamp load, max. Output voltage of or signal "1", min. Output unitage of or signal "1" rated value of or signal "1" rated value of or signal "0" residual current, max. Output delay with resistive load o'0" to "1", max. 10 µA Output delay with resistive load o'0" to "1", max. 15 µs; of the standard outputs, max. (Q 0.2 to Q 1.1) 2 µs; of the pulse outputs, max. (Q 0.0 to Q 0.1) 2 µs o'1" to "0", max. 130 µs; of the standard outputs, max. (Q 0.0 to Q 0.1) 10 µs; of the pulse outputs, max. (Q 0.0 to Q 0.1) 10 µs; of the pu	with resistive load, max.	0.75 A
Output voitage • for signal "1", min. Output current • for signal "4" rated value • for signal "7" residual current, max. Output delay with resistive load • "0" to "1", max. • "1" to "0", max. • "1" to "0", max. 130 µs; of the standard outputs, max. (Q 0.0 to Q 0.1) 2 µs • "1" to "0", max. • for uprating Parallel switching of two outputs • for uprating Switching frequency • of the pulse outputs, with resistive load, max. Total current of the outputs (per group) all mounting positions — up to 40 "C, max. horizontal installation — up to 55 "C, max. Relay outputs • Number of relay outputs • shielded, max. • unshielded, max. • unshielded, max. • unshielded, max. • unshielded, max. • unshielded encoders • 2-wire sensor — permissible quiescent current (2-wire sensor), max. 1. Interface Integrated RS 485 interface Physics Protocols • MPI Yes: As MPI slave for data exchange with MPI masters (S7-30057-400 CPUs, OPs, Tus, Push Button Paneles); \$7-200-		
• for signal "1", min. Cutput current • for signal "1" rated value • for signal "1" residual current, max. 10 µA Output delay with resistive load • "0" to "1", max. • "1" to "0", max. 130 µs; of the standard outputs, max. (Q 0.2 to Q 1.1) 2 µs; of the pulse outputs, max. (Q 0.0 to Q 0.1) 2 µs; of the pulse outputs, max. (Q 0.0 to Q 0.1) 10 µs; of the pulse outputs, max. (Q 0.0 to Q 0.1) 10 µs; of the pulse outputs, max. (Q 0.0 to Q 0.1) 10 µs; of the pulse outputs, max. (Q 0.0 to Q 0.1) 10 µs; of the pulse outputs, max. (Q 0.0 to Q 0.1) 10 µs; of the pulse outputs, max. (Q 0.0 to Q 0.1) 10 µs; of the pulse outputs, with resistive load, max. • for uprating Switching frequency • of the pulse outputs, with resistive load, max. Total current of the outputs (per group) all mounting positions — up to 40 "C, max. • horizontal installation — up to 55 "C, max. 6 A Relay outputs • Number of relay outputs • Shielded, max. • shielded, max. • shielded, max. • unshielded, max. • unshielded, max. 150 m Analog inputs Number of analog potentiometers 2; Analog potentiometer, resolution 8 bit Encoder Connectable encoders • 2-wire sensor — permissible quiescent current (2-wire sensor), max. 1 Interface Physics Protocols • MPI Yes; As MPI slave for data exchange with MPI masters (S7-300/S7-400 CPUs, OPs, TDs, Push Button Paneles); \$7-200-		
Output current • for signal "1" rated value • for signal "0" residual current, max. Output delay with resistive load • "0" to "1", max. 15 µs; of the standard outputs, max. (Q 0.2 to Q 1.1) 2 µs; of the pulse outputs, max. (Q 0.0 to Q 0.1) 2 µs • "1" to "0", max. 15 µs; of the standard outputs, max. (Q 0.2 to Q 1.1) 10 µs; of the pulse outputs, max. (Q 0.0 to Q 0.1) 10 µs Parallel switching of two outputs • for uprating Yes Switching frequency • of the pulse outputs, with resistive load, max. Total current of the outputs (per group) all mounting positions — up to 40 "C, max. horizontal installation — up to 55 "C, max. 6 A Relay outputs • Number of relay outputs • Number of relay outputs Oable length • shielded, max. • unshielded, max. • unshielded, max. 150 m Analog inputs Number of analog potentiometers 2; Analog potentiometer; resolution 8 bit Encoder Connectable encoders • 2-wire sensor — permissible quiescent current (2-wire sensor), max. 1. Interface Interface type Integrated RS 485 interface Physics Protocols • MPI Yes; As MPI slave for data exchange with MPI masters (57-300-57-400 CPUs, OPs, TDs, Push Button Panels); \$7-200-57-400 CPUs, OPs, TDs, Push Button Panels)		20 V DC
 • for signal "1" rated value • for signal "0" residual current, max. 10 μA Output delay with resistive load • "0" to "1", max. • "1" to "0", max. 130 μs; of the standard outputs, max. (Q 0.2 to Q 1.1) 2 μs; of the pulse outputs, max. (Q 0.0 to Q 0.1) 10 μs; of the pulse outputs, max. (Q 0.0 to Q 0.1) 10 μs; of the pulse outputs, max. (Q 0.0 to Q 0.1) 10 μs; of the pulse outputs, max. (Q 0.0 to Q 0.1) 10 μs; of the pulse outputs, max. (Q 0.0 to Q 0.1) 10 μs Parallel switching of two outputs • for uprating Yes Switching frequency • of the pulse outputs, with resistive load, max. Total current of the outputs (per group) all mounting positions — up to 40 "C, max. 6 A horizontal installation — up to 55 "C, max. 6 A Relay outputs • Number of relay outputs 0 Cable length • shelded, max. • unshielded, max. • unshielded, max. • shelded, max. • shelded max. • shelded max. • shelded max. • 100 m Analog inputs Number of analog potentiometers • 2; Analog potentiometer; resolution 8 bit Encoder Connectable encoders • 2-wire sensor — permissible quiescent current (2-wire sensor), max. 1 Interface Integrated RS 485 interface Physics Physics RS 485 Protocols • MPI Yes: As MPI slave for data exchange with MPI masters (S7-300/S7-400 CPUs, OPs, TDs, Push Button Panels); S7-200- 	<u> </u>	20 1 20
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● unshielded, max. Analog inputs Number of analog potentiometers 2; Analog potentiometer; resolution 8 bit Encoder Connectable encoders ● 2-wire sensor — permissible quiescent current (2-wire sensor), max. 1. Interface Interface type Integrated RS 485 interface Physics Protocols ● MPI Yes; As MPI slave for data exchange with MPI masters (S7-300/S7-400 CPUs, OPs, TDs, Push Button Panels); S7-200-	Cable length	
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Number of analog potentiometers 2; Analog potentiometer; resolution 8 bit Encoder Connectable encoders • 2-wire sensor — permissible quiescent current (2-wire sensor), max. 1. Interface Interface type Integrated RS 485 interface Physics Protocols • MPI Yes; As MPI slave for data exchange with MPI masters (S7-300/S7-400 CPUs, OPs, TDs, Push Button Panels); S7-200-	• unshielded, max.	150 m
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Connectable encoders		2; Analog potentiometer; resolution 8 bit
Connectable encoders	Encoder	
— permissible quiescent current (2-wire sensor), max. 1. Interface Interface type Integrated RS 485 interface Physics RS 485 Protocols ■ MPI Yes; As MPI slave for data exchange with MPI masters (S7-300/S7-400 CPUs, OPs, TDs, Push Button Panels); S7-200-		
Interface Interface type Interface type Integrated RS 485 interface Physics RS 485 Protocols • MPI Yes; As MPI slave for data exchange with MPI masters (S7-300/S7-400 CPUs, OPs, TDs, Push Button Panels); S7-200-	• 2-wire sensor	Yes
Interface Interface type Interface type Integrated RS 485 interface Physics RS 485 Protocols • MPI Yes; As MPI slave for data exchange with MPI masters (S7-300/S7-400 CPUs, OPs, TDs, Push Button Panels); S7-200-		1 mA
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Physics RS 485 Protocols • MPI Yes; As MPI slave for data exchange with MPI masters (S7-300/S7-400 CPUs, OPs, TDs, Push Button Panels); S7-200-	1. Interface	
Protocols ◆ MPI Yes; As MPI slave for data exchange with MPI masters (S7-300/S7-400 CPUs, OPs, TDs, Push Button Panels); S7-200-	Interface type	Integrated RS 485 interface
 MPI Yes; As MPI slave for data exchange with MPI masters (S7- 300/S7-400 CPUs, OPs, TDs, Push Button Panels); S7-200- 	Physics	RS 485
300/S7-400 CPUs, OPs, TDs, Push Button Panels); S7-200-	Protocols	
	• MPI	•
internal CPU/CPU communication is possible in the MPI network		
with restrictions; transmission rates: 19.2/187.5 kbit/s		with restrictions, transmission rates: 19.2/187.5 KDIVS

• PPI	Yes; with PPI protocol for program functions, HMI functions (TD 200, OP), S7-200-internal CPU/CPU communication; transmission rates 9.6/19.2/187.5 kbit/s
• serial data exchange	Yes; As freely programmable interface with interrupt facility for serial data exchange with third-party devices with ASCII protocol transfer rates: 1.2 / 2.4 / 4.8 / 9.6 / 19.2 / 38.4 / 57.6 / 115.2 kbps; the PC/PPI cable can also be used as RS 232/RS 485 converter
MPI	
• Transmission rate, min.	19.2 kbit/s
• Transmission rate, max.	187.5 kbit/s

2. Interface	
Interface type	Integrated RS 485 interface
Physics	RS 485
Protocols	
• MPI	Yes; As MPI slave for data exchange with MPI masters (S7-300/S7-400 CPUs, OPs, TDs, Push Button Panels); S7-200-internal CPU/CPU communication is possible in the MPI network with restrictions; transmission rates: 19.2/187.5 kbit/s
• PPI	Yes; with PPI protocol for program functions, HMI functions (TD 200, OP), S7-200-internal CPU/CPU communication; transmission rates 9.6/19.2/187.5 kbit/s
● serial data exchange	Yes; As freely programmable interface with interrupt facility for serial data exchange with third-party devices with ASCII protocol transfer rates: 1.2 / 2.4 / 4.8 / 9.6 / 19.2 / 38.4 / 57.6 / 115.2 kbps; the PC/PPI cable can also be used as RS 232/RS 485 converter

Integrated Functions	
Number of counters	6; High-speed counters (30 kHz each), 32 bit (incl. sign), can be used as up/down counters or for connecting 2 incremental encoders with 2 pulse trains offset by 90° (max. 20 kHz (A/B counters)); parameterizable enable and reset input; interrupt facilities (incl. call of subroutine with any content) when the setpoint is reached; reversal in counting direction, etc.
Counting frequency (counter) max.	30 kHz
Number of alarm inputs	4; 4 rising edges and/or 4 falling edges
Number of pulse outputs	2; High-speed outputs, 20 kHz, with interrupt option; pulse-width and frequency modulation option
Limit frequency (pulse)	20 kHz

Potential separation	
Potential separation digital inputs	
• between the channels	Yes
• between the channels, in groups of	13 and 11
Potential separation digital outputs	
between the channels	Yes; Optocoupler
• between the channels, in groups of	8 and 8

Permissible potential difference	
between different circuits	500 V DC between 24 V DC and 5 V DC
Ambient conditions	
Ambient temperature during operation	
horizontal installation, min.	0°C
• horizontal installation, max.	55 °C
• vertical installation, min.	0°C
• vertical installation, max.	45 °C
Air pressure acc. to IEC 60068-2-13	
permissible range, lower limit	860 hPa
permissible range, upper limit	1 080 hPa
Relative humidity	
Operation, min.	5 %
Operation, max.	95 %; RH class 2 in accordance with IEC 1131-2
Configuration Programming	
Command set	Bit logic instructions, compare instructions, timer instructions,
	counter instructions, clock instructions, transmissions instructions, table instructions, logic instructions, shift and rotate instructions, conversion instructions, program control instructions, interrupt and communications instructions, logic stack instructions, integer maths, floating-point math instructions, numerical functions
Program processing	free cycle (OB 1), interrupt-controller, time-controlled (1 to 255 ms)
 Program organization 	1 OB, 1 DB, 1 SDB subroutines with/without parameter transfer
 Number of subroutines, max. 	64
Programming language	
— LAD	Yes
— FBD	Yes
— STL	Yes
Know-how protection	
User program protection/password protection	Yes; 3-stage password protection
Connection method	
Plug-in I/O terminals	Yes
-	
Dimensions Width	196 mm
Height	196 mm 80 mm
Depth	62 mm
Борит	02 mm
Weights	
Weight, approx.	550 g

08/25/2020 last modified: